

Appl. No. 10/025,256

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Bret S. Weber
Serial No. : 10/025,256
Filed : December 19, 2001
Art Unit : 2182
Examiner : Sorrell, Eron J.
Title : DUAL PORTING SERIAL ADVANCED TECHNOLOGY
ATTACHMENT DISK DRIVES FOR FAULT TOLERANT
APPLICATIONS

**DECLARATION OF PRIOR INVENTION IN THE UNITED STATES OR IN A
NAFTA OR WTO MEMBER COUNTRY TO OVERCOME CITED PATENT OR
PUBLICATION (37 C.F.R. § 1.131)**

Dear Sir:

This declaration is to establish conception of the invention at a date prior to November 30, 2001, which is the earliest effective date of a reference (U.S. Patent Number 6,898,730) cited by the examiner, coupled with diligence in completing the invention in this application in the United States until filing of the patent application on December 19, 2001. This declaration is made after final rejection but immediately following the Examiner's citing of a reference, U.S. Patent No. 6,898,730.

1. The persons making this declaration are the inventors.
2. Conception of the invention occurred prior to the effective date of the reference, November 30, 2001.
3. From the date of conception, the inventor diligently prepared an invention disclosure (Exhibit A).
4. The invention disclosure, prepared by the named inventors was received by the Intellectual Property Department of LSI Logic Corporation as shown on the first page of the invention disclosure (Exhibit A).
5. The invention disclosure was sent to Outside Counsel for draft of the application.
6. The patent application was filed on December 19, 2001.

DECLARATION

As a person signing below:

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Dated: 5/15/06



FULL NAME: Bret S. Weber

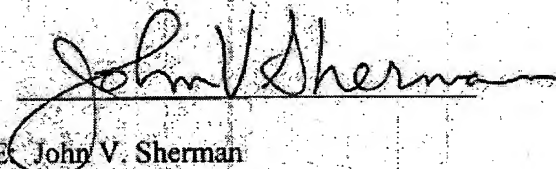
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Dated:

05/12/06



FULL NAME John V. Sherman